# SonetExpert<sup>™</sup> Channelized Analyzer (OC-3/STM-1 and OC-12/STM-4)

**GL** Communications Inc.

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#### Introduction

- Sonet = Synchronous optical networking. Used in North America
- SDH = Synchronous digital hierarchy. Used in the rest of the world
- Sonet and SDH are optical transmission protocols for high-speed data, voice and video traffic
- Data rates
  - Sonet: Optical Carrier (OC) N
  - SDH: Synchronous Transport Module (STM) N
- Sonet/SDH can carry channelized and unchannelized data
  - $\succ$  Channelized = T1 E1
    - OC-3/STM-1 supports 84 T1s or 63 E1s
    - OC-12/STM-4 supports 336 T1s or 252 E1s
  - Unchannelized = Packet over Sonet (PoS), Asynchronous Transfer Mode (ATM)



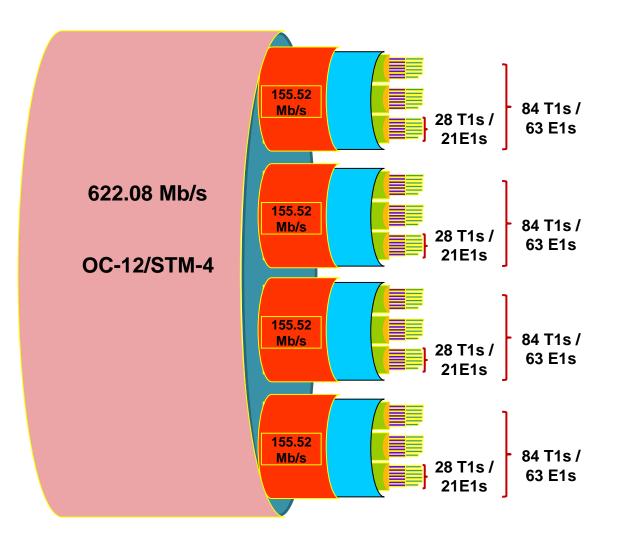
#### **Sonet/SDH Line Rates**

Electrical	Optical (Sonet)	Line Rates	SDH Equivalent
STS-1	OC-1	51.84 Mbps	
STS-3	OC-3	155.52 Mbps	STM-1
STS-9	OC-9	466.56 Mbps	
STS-12	OC-12	622.08 Mbps	STM-4
STS-18	OC-18	933.12 Mbps	
STS-24	OC-24	1.2 Gbps	
STS-36	OC-36	1.9 Gbps	
STS-48	OC-48	2.5 Gbps	STM-16
STS-96	OC-96	5 Gbps	
STS-192	OC-192	10 Gbps	STM-64
STS-768	OC-768	40 Gbps	
STS-3072	OC-3072	160 Gbps	



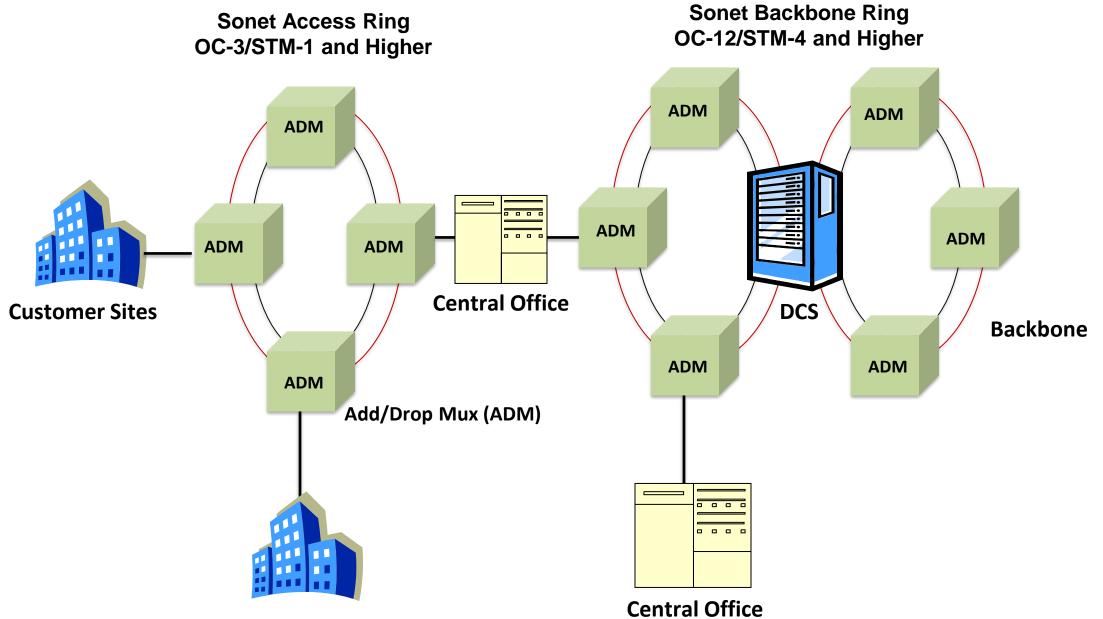
#### Channelized OC-3/STM-1 and OC-12/STM-4

- DS0 = Digital Signal 0 (64 Kbps)
  - Carries digital traffic (including voice)
- T1 = 24 DS0
- E1 = 32 DS0
- OC-3/STM-1 = 84 T1 or 63 E1
- OC-12/STM-4 = 4 STM-1/OC-3
  - ➢ OC-12/STM-4 = 336 T1
  - ➢ OC-12/STM-4 = 252 E1
- OC-12/STM-4 can support ~ 8000 data streams (voice calls)





#### **Sonet Network Elements**





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#### **Sonet/SDH Testing Scenarios**

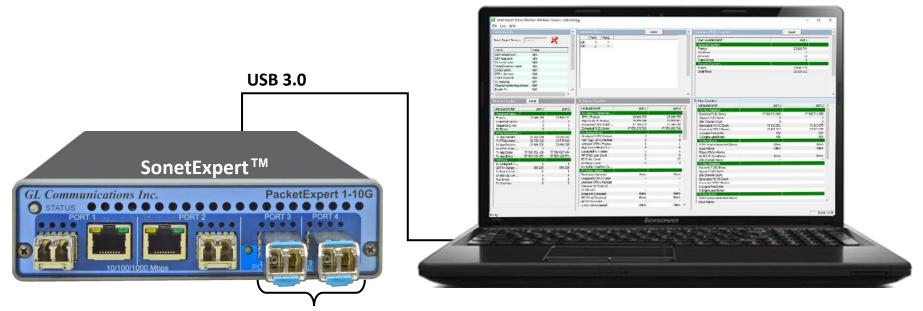
- Monitor T1s, E1s, and DS0s directly without requiring physical access
  - Accessing individual T1s or E1s on a Sonet/SDH link
  - Readily identify traffic types within the complex Sonet/SDH structure
  - > Capturing and analyzing voice calls for call quality or surveillance
- Load testing Sonet/SDH network by generating the maximum number of voice calls/data streams
- Real time alarm detection and management: Send SNMP traps at the individual T1 E1 level for network management



#### SonetExpert<sup>™</sup> Sonet/SDH Channelized Testing Solution



#### **SonetExpert**<sup>™</sup>

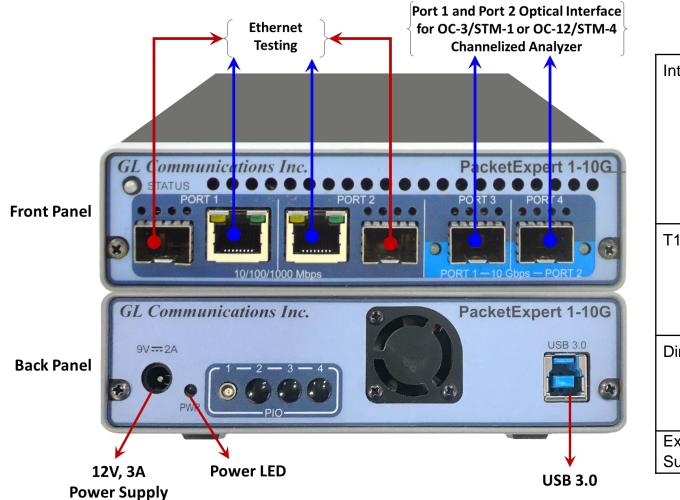


Channelized Ports (Port 1 and Port 2) OC-3/STM-1 or OC-12/STM-4

SonetExpert<sup>™</sup> is configured from a Windows<sup>®</sup> 10 PC via USB 3.0 port



## **SonetExpert™ Portable Hardware Unit**



Interfaces	•	2 x Channelized Ports (OC-3/STM-1 or OC-12/STM-4)
	•	Single Mode or Multi Mode Fiber SFP support with LC
		connector
	•	USB 3.0 Port
	•	External Clock: Input Port 1, Port 2 and Output Port 1,
		Port 2
T1 E1	•	Sync Loss, HDB3 Violation, Carrier Loss, Frame
		Error, Remote, Distant MF, AIS, BPV Errors, CRC
		Errors, Frame Errors, Transmit Under Run, Receive
		Over Run
Dimensions	•	Length: 8.45 in. (214.63 mm)
	•	Width: 5.55 in. (140.97 mm)
	•	Height: 1.60 in (40.64 mm)
External Power Supply	•	+12 Volts (Medical Grade), 3 Amps

## SonetExpert<sup>™</sup> mTOP<sup>™</sup> Probe unit

• PacketExpert<sup>™</sup> hardware is used for both Packet/SonetExpert<sup>™</sup>



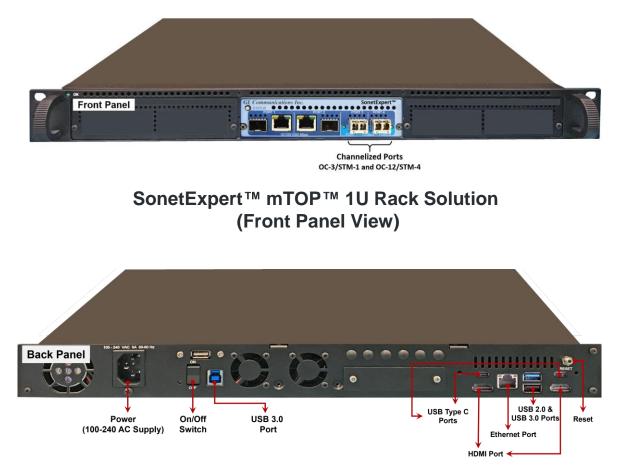
SonetExpert<sup>™</sup> mTOP<sup>™</sup> Probe Solution (Front Panel)



SonetExpert<sup>™</sup> mTOP<sup>™</sup> Probe Solution (Back Panel)

Physical Specifications	<ul> <li>Height: 3.0 Inches (76.2 mm)</li> <li>Length: 10.4 Inches (264.16 mm)</li> <li>Width: 8.4 Inches (213.36 mm)</li> <li>Optional 4-Port SMA Jack Trigger Board (TTL Input/Output)</li> <li>External USB based Wi-Fi adaptor</li> </ul>
SonetExpert™ interfaces (1 unit)	<ul> <li>4x 1G Base-X Optical OR 10/100/1000 Base-T Electrical</li> <li>2x 10G Base-SR, -LR -ER Optical option</li> <li>2 x 100 Mbps Base-FX optical interface</li> <li>Single Mode or Multi Mode Fiber SFP support with LC connector</li> </ul>
External Power Supply	<ul> <li>+12 Volts (Medical Grade), 3 Amps</li> </ul>
SBC Specifications	<ul> <li>AMD Ryzen 9 Series Processor or Intel Core i9 Equivalent</li> <li>Windows® 10 and above 64-bit Pro Operating System</li> <li>USB 2.0 or 3.0 Ports, ATX Power Supply</li> <li>256 GB Hard drive, 8G Memory (Min)</li> <li>Two HDMI ports (Optional VGA to HDMI interface)</li> </ul>

## SonetExpert<sup>™</sup> mTOP<sup>™</sup> 1U Rack Solution



SonetExpert<sup>™</sup> mTOP<sup>™</sup> 1U Rack Solution (Back Panel View)

Physical	Height: 1U Rack
Specifications	Length: 16 Inches
	Width: 19 Inches
	<ul> <li>mTOP<sup>™</sup> System (embedded SBC, 1x SonetExpert<sup>™</sup>)</li> </ul>
SonetExpert™	Two channelized Ports (OC-3/STM-1 or OC-12/STM-4)
interfaces (1 unit)	Single Mode or Multi Mode Fiber SFP support with LC
	connector
SBC Specifications	AMD Ryzen 9 Series Processor or Intel Core i9 Equivalent
	Windows® 10 and above 64-bit Pro Operating System
	USB 2.0 or 3.0 Ports, ATX Power Supply
	USB Type C ports, Ethernet 2.5GigE port
	256GB Hard drive, 8G Memory (Min)



#### **SonetExpert™** Features

- 2 Channelized Ports:
  - > OC-3/STM-1 or OC-12/STM-4 interfaces
  - Simulate and monitor in both directions
- Configure the number of T1 E1 channels to be Multiplexed or Demultiplexed
- Analyze/emulate voice, data, fax, protocols, analog and digital signals, including echo and voice quality
- Comprehensive protocol analysis and emulation HDLC, SS7, ISDN, CAS, PPP, Frame Relay, ATM and more
- Capture, transmit and process at wirespeed
- Broadcasts the selected T1 E1 channel data on all the 252 E1's or 336 T1's
- Direct access to any or all T1s and E1s
  - ➤ 2 x 336 T1's x 24 = 16,128 DS0s
  - ➤ 2 x 252 E1's x 31 = 15,624 DS0s

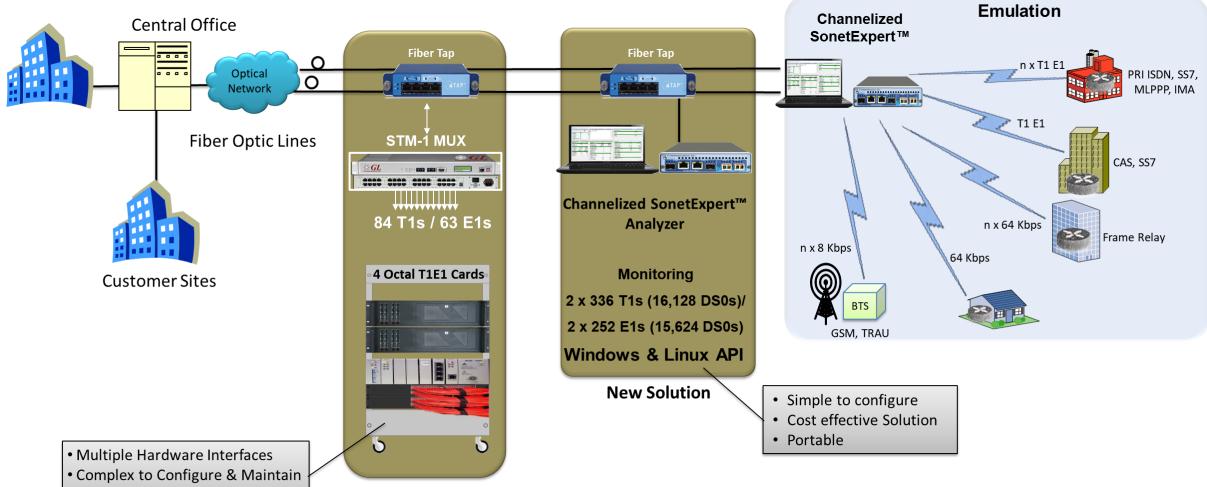


#### SonetExpert<sup>™</sup> Features (Contd.)

- Pluggable SFPs allow Single Mode (SM), and Multi-mode (MM) fiber optic non-intrusive tap
- Supports any combination of DS0/64/56/16/8 kbps fractional T1 E1, and N x T1 E1 interface definitions (a total of 252 E1s or 336 T1s – in each port)
- Provides Loss of Signal (LOS) and Loss of Frames (LOF) Hardware Alarms indication, Service logging, External Clock, Line and Diagnostic Loopback options, Through mode and Port Swap Cross-port options
- Supports multiplexing multiple T1 or E1 channels to a single channelized OC-3/12 STM-1/4 line
- User configurable OC-3/STM-1 or OC-12/STM-4 mapping
- Provides an option to restart the SEC service automatically



## **Channelized T1 E1 Monitoring**



• Space Consuming & Expensive

**Traditional Solution** 



## SonetExpert<sup>™</sup> Analyzer GUI

#### Monitor all T1s or E1s

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Communications

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# **Protocol Analyzers**

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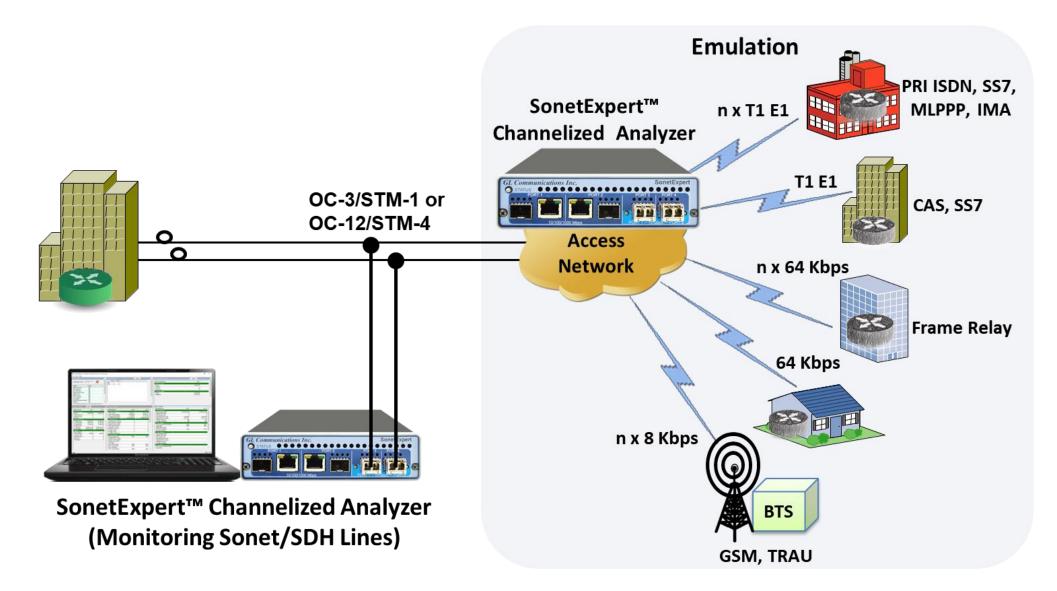
**Communications** 

#### **ISDN Call Capture and Analysis**

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IEI Channel Identification = 00011000 Channel Identification IE Identi	00.0 00.0 00.0 00.0 01.0 01.0	0.986 19 0.989 20 0.990 21 1.153 40 1.168 66 1.325 73	1:16 1:16 1:16 1:16 1:16 1:16	CALL A CONNECT DIS	PROCEEDING LERTING ONNECT ACKNOWLEDGI SCONNECT IELEASE		216 216 216 216 216 216		SADI TEI Ctl N(S) P N(R) Protocol Call Ref Call Ref Call Ref Call Ref	Discriminator Grence Length Grence Value Grence Flag Type El Bearer Capabili S Bearer Capabili nformation Transfe Coding Standard nformation Transfe Transfer Mode Ser Information L Ser Information L El Channel Identi:	ity by Length er Capabili er Rate ayer 1 Prot syer 1 Prot fication	ty ocol (LL ocol Ide	= 000000 (0) = 0000000. (0) =0 Info = 0000000. (0) =0 (0) = 00000000. (0) = 00000000. (0) = 00001000 Q931 =0010 (2) = 2 (.0000000 0 = 0 FROM = 00000101 SETU = 00000101 SETU = 00000101 SETU =00000 Spee = .00 ITU =00001 A-1 = 0.0011000 Cham	rmation /I.451 user-n 0000010) side that or P er Capability ch I (CCIIT) sta bit/s uit Mode aw Rec G.711	etwork call o iginated call IE Identifie ndardized cod	ioni ire: iini
										E Channel Identif:	cation Len	ath	= 3 (x03)			



#### SonetExpert<sup>™</sup> Channelized T1 E1 Emulation

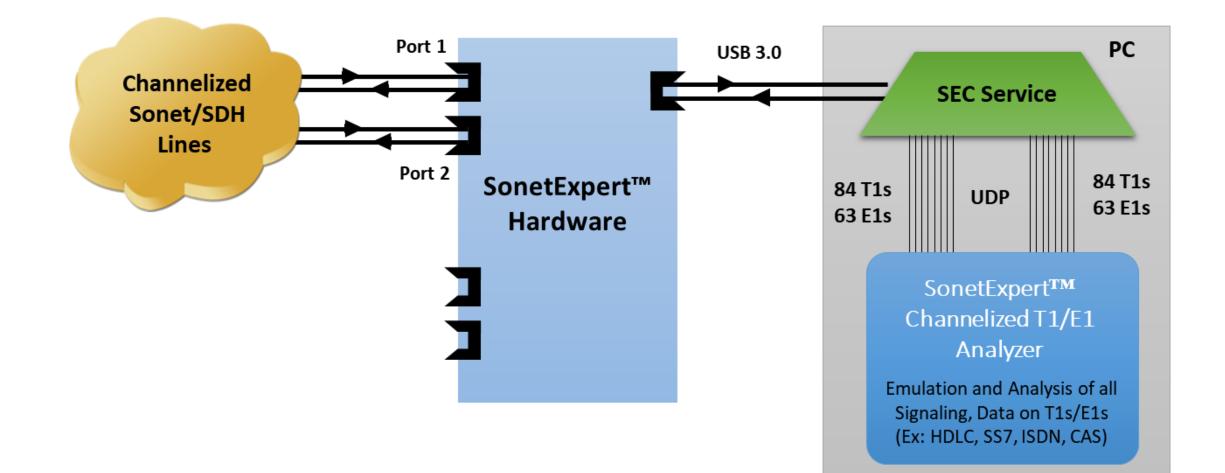




#### MAPS<sup>™</sup> Call Generation, Reception, and Statistics

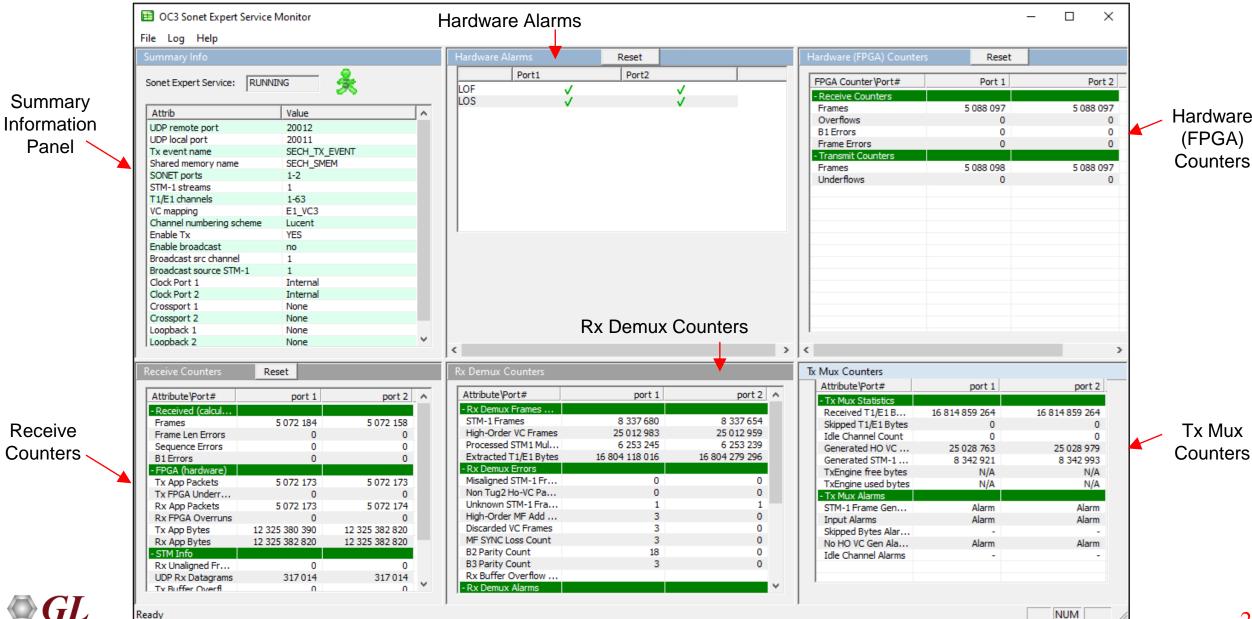
MAPS (Message Automation Protocol Simulat Configurations Emulator Reports Editor		n de la constante de la consta La constante de la constante de	orts-163]			-	- # ×						
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2 Isup_Call.gls Card1TS02 21.2,1.1.1, 3 Isup_Call.gls Card1TS03 21.2,1.1.1,		Transmitting File Transmitting File	Terminate Cal Terminate Cal	600					· Hardele abot to observe robust	- and the state of the state	-		
4 Isup_Call.gls Card1TS04 21.2,1.1.1.4		Transmitting File	Teminate Ca			Protocol Simulation) SSP					- 0	×	
5 Isup_Call.gl: Card1T505 2.1.2.1.1.1.5	The second se	Transmitting File	Teminate Ca	Configuration	ons Emulator	Reports Editor Debug	Tools Windows	Help				- 8 ×	
6 Isup_CalligIs Card1TS06 21.2.1.1.1.6 7 Isup_CalligIs Card1TS07 21.2.1.1.1.7	Stop	Transmitting File Transmitting File	Teminate Ca Teminate Ca	Q 🗐 🕄	600	🛯 🤗 🏂 🧭 🕯	2 👌 ò ò	2 🔒 🔮					
8 Isup_Call.gls Card1T508 21.2.1.1.1.9 9 Isup_Call.gls Card1T509 21.2.1.1.1.9		Transmitting File Transmitting File	Terminate Ca Terminate Ca	SrNo S	icript Name	Profile (	Call Info	Script Execution	Status	Events	Events Profile Results	~	
10 Isup_Callgli Card17510 21.2.1.1.1		Transmitting File	Teminate Ca	7	SLTM.gls		1.7.1,2.7.2,1	Stop	MTP3 Activ		Pass	D	
11 Isup_Call.gls Card1TS11 2.1.2.1.1.1.1		Transmitting File	Terminate Ca	8	SLTM.gls		1.81,282,1	Stop	MTP3 Activ File Sent		Pass Pass		
				10	lsup_Callg lsup_Callg		1.1.2.1.2.2	Stop		Terminate Call Terminate Call	Pass	and a second	
Add Delete Insert Refresh Sat	Start All Stop 🔻 St	op All V Abort	Abort All	11	lup_Callg		1.1.1.2.1.2.3	Stop	File Sent	Terminate Call	Patt	444 I.A.	
Save Column Width	F Show Latest			12	ltup_Call.g	lt Card1TS04	1.1.1.2.1.2.4	Stop	File Sent	Terminate Call	Patt		
Save commission	In allow Edicia		- 0	13	lsup_Callg		1.1.1.2.1.2.5	Stop	File Sent	Terminate Call	Pass		
MAPS	DUT			14	Isup_Callg Isup_Callg		1.1.1,2.1.2,6 1.1.1,2.1.2,7	Stop	File Sent	Terminate Call Terminate Call	Pass Pass		
Initial Addre	ess 17.12.57	222 713			http_carg			5109	File Service	Terrinde Car		· · · · · · · · · · · · · · · · · · ·	
Address Com	alata and a state			Stop	Stop Al Abort	Abort All 🔽 Show Res	cords T Select Active	e Call 🔽 Auto Trash T	Trash				
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Answer	171258	176.5901		Save	Column Width	-j I₹ Show	v Latest						
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						- Salar Stranger Stranger	Statistic N	ame Total Calls		eted Calls Passed Calls	Faled Cals Cals/Se	c	
¢			>				Default	343988	1898 34209	0 342090	0 0		
Scripts Message Sequence Event Con	fig \ Script Flow /			<									
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										Service States			
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Communications										Initialisation Errors	Error Events	Captured Errors	Link Status Up=8 Down=0 //

## **Working Principle**





#### SonetExpert<sup>™</sup> Monitor and Control Application (OC-3)



Counters



#### SonetExpert<sup>™</sup> Monitor and Control Application (STM-1)

Log Help								
nmary Info			Hardware Alarms			Hardware (FPGA) Counters		et
net Expert Service: RUN	INING 🔶		Port1 Port2			FPGA Counter\Port#	Port 1	Po
			LOF V V LOS V V			- Receive Counters		
ttrib	Value		LOS 🗸 🗸			Frames	667 350 651	667 350
OP remote port	20012					Overflows	0	
OP local port	20011					B1 Errors Frame Errors	0	
event name	SECH TX EVE	NT				- Transmit Counters	0	
nared memory name	SECH_SMEM					- Transmit Counters Frames	667 350 650	667 350
)H ports	1-2					Underflows	007 350 650	007 330
M-1 streams	1					- Rx/Tx Frequency	0	
I/E1 channels	1-63					Rx Freq	155 520 000	155 520
C mapping	E1_VC3					Tx Freq	155 520 000	155 520
nannel numbering scheme	Lucent					IXTICY	133 320 000	155 520
nable Tx	YES		,					
nable broadcast	no							
oadcast src channel	1							
oadcast source STM-1	1							
ock Port 1	Internal							
ock Port 2	Internal							
ossport 1	None							
ossport 2	None							
opback 1	None							
opback 2	None							
	Reset		Rx Demux Counters			Tx Mux Counters	port 1	po
ttribute\Port#		port 2	Attribute\Port#	port 1	port 2	Attribute\Port# - Tx Mux Statistics		
ttribute\Port# Received (calculated)	Reset port 1		Attribute\Port#			Attribute\Port#  Tx Mux Statistics Received T1/E1 Bytes	1 429 919 834 112	
ttribute \Port# Received (calculated)	Port 1 667 350 502	667 350 502	Attribute\Port# - Rx Demux Frames Received STM-1 Frames	709 321 261	709 316 270	Attribute \Port# -Tx Mux Statistics Received T1/E1 Bytes Skipped T1/E1 Bytes	1 429 919 834 112 0	· · · · · · · · · · · · · · · · · · ·
ttribute\Port# Received (calculated) Frames Frame Len Errors	Port 1 667 350 502 0	667 350 502 0	Attribute\Port# - Rx Demux Frames Received STM-1 Frames High-Order VC Frames	709 321 261 2 127 953 685	709 316 270 2 127 938 811	Attribute\Port# - Tx Mux Statistics Received T1/E1 Bytes Skipped T1/E1 Bytes Idle Channel Count	1 429 919 834 112 0 0	1 429 919 834 :
ttribute \Port# Received (calculated) Frames Frame Len Errors Sequence Errors	Reset port 1	667 350 502 0 0	Attribute\Port# -Rx Demux Frames Received STM-1 Frames High-Order VC Frames Processed OC-3/STM-1 Mul	709 321 261 2 127 953 685 531 988 419	709 316 270 2 127 938 811 531 984 702	Attribute\Port# -Tx Mux Statistics Received T1/E1 Bytes Skipped T1/E1 Bytes Idle Channel Count Generated HO STS/VC C	1 429 919 834 112 0 0 2 127 998 343	1 429 919 834 1 2 127 998 2
ttribute\Port# Received (calculated) Frames Frame Len Errors Biguence Errors Bi Errors	Port 1 667 350 502 0	667 350 502 0	Attribute \Port# Rx Demux Frames Received STM-1 Frames High-Order VC Frames Processed OC-3/STM-1 Mul Extracted T1/E1 Bytes	709 321 261 2 127 953 685	709 316 270 2 127 938 811	Attribute\Port# - TX MUX Statistics Received T1/E1 Bytes Skipped T1/E1 Bytes Idle Channel Count Generated HO STS/VC C Generated HO STS/VC C	1 429 919 834 112 0 0 2 127 998 343 709 332 781	1 429 919 834 1 2 127 998 2 709 332 7
ttribute\Port# Received (calculated) Frames Frame Len Errors Biguence Errors Bi Errors PPGA (hardware)	Reset port 1 667 350 502 0 0	667 350 502 0 0 0	Attribute\Port# - Rx Demux Frames Received STM-1 Frames High-Order VC Frames Processed 0C-3/STM-1 Mul Extracted T1/E1 Bytes - Rx Demux Errors	709 321 261 2 127 953 685 531 988 419 1 429 899 633 792	709 316 270 2 127 938 811 531 984 702 1 429 899 674 112	Attribute\Port# -Tx Mux Statistics Received T1/E1Bytes Skipped T1/E1Bytes Idle Channel Count Generated HO STS/VC C Generated OC-3/STM-1 TxEngine free bytes	1 429 919 834 112 0 2 127 998 343 709 332 781 N/A	1 429 919 834 : 2 127 998 2 709 332 :
ttribute\Port# Received (calculated) Trames Trame Len Errors Sequence Errors 11 Errors FPGA (hardware) X App Packets	Reset port 1 667 350 502 0 0 0 667 350 502	667 350 502 0 0 667 350 502	Attribute\Port# - Rx Demux Frames Received STM-1 Frames High-Order VC Frames Processed OC-3/STM-1 Mul Extracted T1/E1 Bytes - Rx Demux Errors Misaligned OC-3/STM-1 Fra	709 321 261 2 127 953 685 531 988 419 1 429 899 633 792 0	709 316 270 2 127 938 811 531 984 702 1 429 899 674 112 0	Attribute\Port# - Tx Mux Statistics Received T1/E1Bytes Skipped T1/E1Bytes Idle Channel Count Generated HO STS/VC C Generated OC-3/STM-1 TxEngine free bytes TxEngine used bytes	1 429 919 834 112 0 0 2 127 998 343 709 332 781	1 429 919 834 2 127 998 709 332
ttribute 'Port# Received (calculated) Frames Frame Len Errors Sequence Errors S1 Errors FPGA (hardware) Tx App Packets Tx App Ackets Tx FPGA Underruns	Reset port 1 667 350 502 0 667 350 502 0 667 350 502 0	667 350 502 0 0 0 0 0 667 350 502 0	Attribute\Port# - Rx Demux Frames Received STM-1 Frames High-Order VC Frames Processed OC-3/STM-1 Mul Extracted T1/E1 Bytes - Rx Demux Errors Misaligned OC-3/STM-1 Fra Non Tug2 Ho-STS/VC Payload	709 321 261 2 127 953 685 5 31 988 419 1 429 899 633 792 0 0	709 316 270 2 127 938 811 531 984 702 1 429 899 674 112 0 0	Attribute\Port# - Tx Mux Statistics Received T1/E1 Bytes Skipped T1/E1 Bytes Idle Channel Count Generated HO STS/VC C Generated OC-3/STM-1 TxEngine free bytes TxEngine used bytes - Tx Mux Alarms	1 429 919 834 112 0 2 127 998 343 709 332 781 N/A N/A	1 429 919 834 2 127 998 709 332
ttribute\Port# Received (calculated) Frames Frame Len Errors Sequence Errors 31 Errors FPGA (hardware) Tx App Packets Tx FPGA Underruns Tx App Packets	Reset port 1 667 350 502 0 667 350 502 0 667 350 502 0 667 350 502 0 667 350 502	667 350 502 0 0 667 350 502 0 667 350 502	Attribute\Port# - Rx Demux Frames Received STM-1 Frames High-Order VC Frames Processed OC-3/STM-1 Mul Extracted T1/E1 Bytes - Rx Demux Errors Misaligned OC-3/STM-1 Fra Non Tug2 Ho-STS/VC Payload Unknown OC-3/STM-1 Fra	709 321 261 2 127 953 685 531 988 419 1 429 899 633 792 0	709 316 270 2 127 938 811 531 984 702 1 429 899 674 112 0 0 1	Attribute\Port#  Tx Mux Statistics Received T1/E1 Bytes Skipped T1/E1 Bytes Idle Channel Count Generated HO STS/VC C Generated OC -3/STM-1 TxEngine free bytes TxEngine used bytes TxEngine used bytes OC -3/STM-1 Frame Gene	1 429 919 834 112 0 2 127 998 343 709 332 781 N/A N/A Alarm	1 429 919 834 3 2 127 998 3 709 332 3 1 1 1
tribute\Port# Received (calculated) rames rame Len Errors B1 Errors PGA (hardware) Tx App Packets Tx FPGA Underruns Xx App Packets Xx FPGA Overruns	Reset port 1 667 350 502 0 0 0 667 350 502 0 667 350 502 0 667 350 502 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	667 350 502 0 0 667 350 502 0 667 350 502 0	Attribute\Port# - Rx Demux Frames Received STM-1 Frames High-Order VC Frames Processed OC-3/STM-1 Mul Extracted T1/E1 Bytes - Rx Demux Errors Misaligned OC-3/STM-1 Fra Non Tug2 Ho-STS/VC Payload Unknown OC-3/STM-1 Fra High-Order MF Add Count	709 321 261 2 127 953 685 531 988 419 1 429 899 633 792 0 0 1 1 6	709 316 270 2 127 938 811 531 984 702 1 429 899 674 112 0 0 1 3	Attribute\Port#  TX Mux Statistics Received T1/E1 Bytes Skipped T1/E1 Bytes Idle Channel Count Generated HO STS/VC C Generated HO STS/VC C TxEngine free bytes TxEngine used bytes TxEngine used bytes TxEngine used bytes ITX Mux Alarms OC-3/STM-1 Frame Gene Input Alarms	1 429 919 834 112 0 2 127 998 343 709 332 781 N/A N/A	1 429 919 834 1 2 127 998 2 709 332 7 N
ttribute \Port # Received (calculated) rrames Len Errors iequence Errors PGA (hardware) ix App Packets ix FPGA Underruns tx App Packets tx FPGA Overruns ix App Qakets tx FPGA Overruns ix App Stes	Reset port 1 667 350 502 0 667 350 502 0 667 350 502 0 667 350 502 0 1 621 661 717 430	667 350 502 0 0 0 0 667 350 502 667 350 502 0 1 621 661 722 290	Attribute\Port# - Rx Demux Frames Received STM-1 Frames High-Order VC Frames Processed OC-3/STM-1 Mul Extracted T1/E1 Bytes - Rx Demux Errors Misaligned OC-3/STM-1 Fra Non Tug2 Ho-STS/VC Payload Unknown OC-3/STM-1 Fra	709 321 261 2 127 953 685 531 988 419 1 429 899 633 792 0 0 1	709 316 270 2 127 938 811 531 984 702 1 429 899 674 112 0 0 1	Attribute\Port# - Tx Mux Statistics Received T1/E1 Bytes Skipped T1/E1 Bytes Idle Channel Count Generated HO STS/VC C Generated OC-3/STM-1 TxEngine free bytes TxEngine used bytes - Tx Mux Alarms OC-3/STM-1 Frame Gene Input Alarms Skipped Bytes Alarms	1 429 919 834 112 0 2 127 998 343 709 332 781 N/A N/A Alarm Alarm	1 429 919 834 1 2 127 998 2 709 332 7 N Ala Ala
ttribute\Port#  Received (calculated)  irames  irame Len Errors  equence Errors  PGA (hardware)  x App Packets  x App Pytes  x App Bytes  x App Bytes	Reset port 1 667 350 502 0 0 0 667 350 502 0 667 350 502 0 667 350 502 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	667 350 502 0 0 667 350 502 0 667 350 502 0	Attribute\Port# -Rx Demux Frames Received STM-1 Frames High-Order VC Frames Processed OC-3/STM-1 Mul Extracted T1/E1 Bytes -Rx Demux Errors Misaligned OC-3/STM-1 Fra Non Tug2 Ho-STS/VC Payload Unknown OC-3/STM-1 Fra High-Order MF Add Count Discarded STS/VC Frames	709 321 261 2 127 953 685 5 31 988 419 1 429 899 633 792 0 0 1 6 6 6	709 316 270 2 127 938 811 531 984 702 1 429 899 674 112 0 0 1 3 3 3	Attribute\Port#  Tx Mux Statistics Received T1/E1 Bytes Skipped T1/E1 Bytes Idle Channel Count Generated HO STS/VC C Generated OC-3/STM-1 TxEngine free bytes TxEngine used bytes TxEngine used bytes Tx Mux Alarms OC-3/STM-1 Frame Gene Input Alarms Skipped Bytes Alarms No HO STS/VC Gen Alarms	1 429 919 834 112 0 2 127 998 343 709 332 781 N/A N/A Alarm	1 429 919 834 3 2 127 998 2 709 332 7 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
tribute\Port#  keceived (calculated) rames rame Len Errors equence Errors 1 Errors PGA (nardware) X App Packets X FPGA Underruns X App Ackets X FPGA Overruns X App Bytes X App Bytes QNET/SDH Info	Reset port 1 667 350 502 0 0 0 667 350 502 0 667 350 502 0 667 350 502 0 1 621 661 717 430 1 621 661 719 860	667 350 502 0 0 0 0 667 350 502 667 350 502 0 1 621 661 722 290	Attribute\Port# - Rx Demux Frames Received STM-1 Frames High-Order VC Frames Processed OC-3/STM-1 Mul Extracted T1/E1 Bytes - Rx Demux Errors Misaligned OC-3/STM-1 Fra Non Tug2 Ho-STS/VC Payload Unknown OC-3/STM-1 Fra High-Order MF Add Count Discarded STS/VC Frames MF SYNC Loss Count	709 321 261 2 127 953 685 531 988 419 1 429 899 633 792 0 0 1 1 6 6 6	709 316 270 2 127 938 811 5 31 984 702 1 429 899 674 112 0 0 1 3 3 3 3	Attribute\Port# - Tx Mux Statistics Received T1/E1 Bytes Skipped T1/E1 Bytes Idle Channel Count Generated HO STS/VC C Generated OC-3/STM-1 TxEngine free bytes TxEngine used bytes - Tx Mux Alarms OC-3/STM-1 Frame Gene Input Alarms Skipped Bytes Alarms	1 429 919 834 112 0 2 127 998 343 709 332 781 N/A N/A Alarm Alarm	1 429 919 834 1 2 127 998 2 709 332 7 N Ala Ala
ttribute\Port# Received (calculated) rames Len Errors rames Len Errors PGA (hardware) Tx App Packets Tx FPGA Underruns Rx App Packets Rx FPGA Overruns Tx App Bytes Sx App Bytes Sx App Bytes Sx Unaligned Frames	Reset port 1 667 350 502 0 0 667 350 502 0 667 350 502 0 667 350 502 0 1 621 661 717 430 1 621 661 719 860 0 0 0 0 0 0 0 0 0	667 350 502 0 0 0 667 350 502 0 667 350 502 0 1 621 661 722 290 1 621 661 722 290 0	Attribute\Port# -Rx Demux Frames Received STM-1 Frames High-Order VC Frames Processed OC-3/STM-1 Mul Extracted T1/E1 Bytes -Rx Demux Errors Misaligned OC-3/STM-1 Fra Non Tug2 Ho-STS/VC Payload Unknown OC-3/STM-1 Fra High-Order MF Add Count Discarded STS/VC Frames MF SYNC Loss Count B2 Parity Count	709 321 261 2 127 953 685 531 988 419 1 429 899 633 792 0 0 1 1 6 6 6 6 6 6 3 365	709 316 270 2 127 938 811 531 984 702 1 429 899 674 112 0 0 1 3 3 3 3 3 3 3 3 3	Attribute\Port#  Tx Mux Statistics Received T1/E1 Bytes Skipped T1/E1 Bytes Idle Channel Count Generated HO STS/VC C Generated OC-3/STM-1 TxEngine free bytes TxEngine used bytes TxEngine used bytes Tx Mux Alarms OC-3/STM-1 Frame Gene Input Alarms Skipped Bytes Alarms No HO STS/VC Gen Alarms	1 429 919 834 112 0 2 127 998 343 709 332 781 N/A N/A Alarm Alarm	1 429 919 834 1 2 127 998 2 709 332 7 N N Ala Ala
tribute \Port # keceived (calculated) rames rame Len Errors equence Errors 1 Errors PGA (hardware) x App Packets x App Packets x FPGA Underruns x App Packets x FPGA Overruns x App Bytes x App Bytes x App Bytes X App Bytes ONET/SDH Info x Unaligned Frames DP Rx Datagrams	Reset port 1 667 350 502 0 0 0 667 350 502 0 667 350 502 0 667 350 502 0 1 621 661 717 430 1 621 661 719 860	667 350 502 0 0 667 350 502 0 667 350 502 0 1 621 661 722 290 1 621 661 722 290	Attribute\Port# Rx Demux Frames Received STM-1 Frames High-Order VC Frames Processed OC-3/STM-1 Mul Extracted T1/E1 Bytes Rx Demux Errors Misaligned OC-3/STM-1 Fra Non Tug2 Ho-STS/VC Payload Unknown OC-3/STM-1 Fra High-Order MF Add Count Discarded STS/VC Frames MF SYNC Loss Count B2 Parity Count B3 Parity Count	709 321 261 2 127 953 685 531 988 419 1 429 899 633 792 0 0 1 1 6 6 6 6 6 6 3 365	709 316 270 2 127 938 811 531 984 702 1 429 899 674 112 0 0 1 3 3 3 3 3 3 3 3 3	Attribute\Port#  Tx Mux Statistics Received T1/E1 Bytes Skipped T1/E1 Bytes Idle Channel Count Generated HO STS/VC C Generated OC-3/STM-1 TxEngine free bytes TxEngine used bytes TxEngine used bytes Tx Mux Alarms OC-3/STM-1 Frame Gene Input Alarms Skipped Bytes Alarms No HO STS/VC Gen Alarms	1 429 919 834 112 0 2 127 998 343 709 332 781 N/A N/A Alarm Alarm	1 429 919 834 1 2 127 998 2 709 332 7 N Ala Ala
tribute\Port# keceived (calculated) rames rame Len Errors equence Errors PGA (hardware) x App Packets x FPGA Underruns x App Packets x App Bytes x App Bytes x App Bytes ONET/SDH Info x Unaligned Frames IDP Rx Datagrams x Buffer Overflows	Reset port 1 667 350 502 0 667 350 502 0 667 350 502 0 667 350 502 0 1621 661 717 430 1621 661 719 860 0 0 1621 661 719 800 0 0 1621 661 719 800 0 0 0 1621 661 719 800 0 0 0 1621 661 719 800 0 0 1621 661 719 800 0 0 0 1621 661 719 800 0 0 0 0 1621 661 719 800 0 0 0 0 1621 661 719 800 0 0 0 0 1621 661 719 800 0 0 0 0 0 1621 661 719 800 0 0 0 0 0 0 0 0 0	667 350 502 0 0 0 667 350 502 0 667 350 502 0 1 621 661 722 290 1 621 661 722 290 1 621 661 722 290 0 41 709 408	Attribute\Port#  - Rx Demux Frames Received STM-1 Frames High-Order VC Frames Processed OC-3/STM-1 Mul Extracted T1/E1 Bytes - Rx Demux Errors Misaligned OC-3/STM-1 Fra Non Tug2 Ho-STS/VC Payload Unknown OC-3/STM-1 Fra High-Order MF Add Count Discarded STS/VC Frames MF SYNC Loss Count B2 Parity Count B3 Parity Count Rx Buffer Overflow Count	709 321 261 2 127 953 685 531 988 419 1 429 899 633 792 0 0 1 1 6 6 6 6 6 6 3 365	709 316 270 2 127 938 811 531 984 702 1 429 899 674 112 0 0 1 3 3 3 3 3 3 3 3 3	Attribute\Port#  Tx Mux Statistics Received T1/E1 Bytes Skipped T1/E1 Bytes Idle Channel Count Generated HO STS/VC C Generated OC-3/STM-1 TxEngine free bytes TxEngine used bytes TxEngine used bytes Tx Mux Alarms OC-3/STM-1 Frame Gene Input Alarms Skipped Bytes Alarms No HO STS/VC Gen Alarms	1 429 919 834 112 0 2 127 998 343 709 332 781 N/A N/A Alarm Alarm	1 429 919 834 1 2 127 998 2 709 332 7 N Ala Ala
ttribute \Port #  Received (calculated)  rames rame Len Errors rame Len Errors (1 Errors PGA (hardware) X App Packets X FPGA Overruns X App Packets X FPGA Overruns X App Bytes X App Byte	Reset port 1 667 350 502 0 0 0 667 350 502 0 667 350 502 0 667 350 502 0 1 621 661 717 430 1 621 661 719 860 0 41 709 408 0	667 350 502 0 0 667 350 502 0 667 350 502 0 1 621 661 722 290 1 621 661 722 290 0 41 709 408 0	Attribute\Port#  Attribute\Port#  Attribute\Port#  Attribute\Port#  Attribute\Port#  High-Order VC Frames  Processed OC-3/STM-1 Mul Extracted T1/E1 Bytes  Attribute  Attribu	709 321 261 2 127 953 685 531 988 419 1 429 899 633 792 0 0 1 1 6 6 6 6 6 6 3 365 3	709 316 270 2 127 938 811 5 31 984 702 1 429 899 674 112 0 0 1 1 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	Attribute\Port#  Tx Mux Statistics Received T1/E1 Bytes Skipped T1/E1 Bytes Idle Channel Count Generated HO STS/VC C Generated OC-3/STM-1 TxEngine free bytes TxEngine used bytes TxEngine used bytes Tx Mux Alarms OC-3/STM-1 Frame Gene Input Alarms Skipped Bytes Alarms No HO STS/VC Gen Alarms	1 429 919 834 112 0 2 127 998 343 709 332 781 N/A N/A Alarm Alarm	1 429 919 834 1 2 127 998 2 709 332 7 N Ala Ala
ttribute \Port #  keceived (calculated)  rames rame Len Errors kequence Errors H1 Errors PGA (hardware) X App Packets X App Packets X App Ackets X App Ackets X App Ackets X App Bytes SONET/SDH Info XX Unaligned Frames IDP Rx Datagrams X Buffer Overflows Invalid Udp LoPorts Mux Errors	Reset port 1 667 350 502 0 0 0 667 350 502 0 667 350 502 0 667 350 502 0 1621 661 717 430 1621 661 719 860 0 41 709 408 0 0 0 0 0 0 0 0 0	667 350 502 0 0 667 350 502 0 667 350 502 0 1 621 661 722 290 1 621 661 722 290 0 41 709 408 0 0	Attribute\Port# - Rx Demux Frames Received STM-1 Frames High-Order VC Frames Processed OC-3/STM-1 Mul Extracted T1/E1 Bytes - Rx Demux Errors Misaligned OC-3/STM-1 Fra Non Tug2 Ho-STS/VC Payload Unknown OC-3/STM-1 Fra High-Order MF Add Count Discarded STS/VC Frames MF SYNC Loss Count B2 Parity Count B3 Parity Count Rx Buffer Overflow Count - Rx Demux Alarms No Frames Received Unaligned OC-3/STM-1 Fra Unknown OC-3/STM-1 Fra	709 321 261 2 127 953 685 531 988 419 1 429 899 633 792 0 0 1 1 6 6 6 6 6 6 3 365 3	709 316 270 2 127 938 811 5 31 984 702 1 429 899 674 112 0 0 1 1 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	Attribute\Port#  Tx Mux Statistics Received T1/E1 Bytes Skipped T1/E1 Bytes Idle Channel Count Generated HO STS/VC C Generated OC-3/STM-1 TxEngine free bytes TxEngine used bytes TxEngine used bytes Tx Mux Alarms OC-3/STM-1 Frame Gene Input Alarms Skipped Bytes Alarms No HO STS/VC Gen Alarms	1 429 919 834 112 0 2 127 998 343 709 332 781 N/A N/A Alarm Alarm	1 429 919 834 1 2 127 998 2 709 332 7 N Ala Ala
ttribute \Port #  Received (calculated)  rames rame Len Errors equence Errors PGA (hardware)  x App Packets x App Packets x App Packets x FPGA Overruns x App Bytes SONET/SDH Info tx Unaligned Frames JDP Rx Datagrams x Buffer Overflows nvalid Udp LoPorts Iux Erros	Reset port 1 667 350 502 0 667 350 502 0 667 350 502 0 667 350 502 0 1 621 661 717 430 1 621 661 719 860 0 41 709 408 0 0 0 0 0 0 0 0 0	667 350 502 0 0 0 667 350 502 0 667 350 502 0 1 621 661 722 290 1 621 661 722 290 1 621 661 722 290 0 41 709 408 0 0 0	Attribute\Port#         -Rx Demux Frames Received         STM-1 Frames         High-Order VC Frames         Processed OC-3/STM-1 Mul         Extracted T1/E1 Bytes         -RX Demux Errors         Misaligned OC-3/STM-1 Fra         Non Tug2 Ho-STS/VC Payload         Unknown OC-3/STM-1 Fra         High-Order MF Add Count         Discarded STS/VC Frames         MF SYNC Loss Count         B3 Parity Count         B3 Parity Count         B3 Parity Count         B3 Parity Count         No Frames Received         Unaligned OC-3/STM-1 Fra         Unknown OC-3/STM-1 Fra         Unknown STS/VC Payload	709 321 261 2 127 953 685 5 31 988 419 1 429 899 633 792 0 0 0 1 1 6 6 6 6 6 3 365 3 3	709 316 270 2 127 938 811 5 31 984 702 1 429 899 674 112 0 0 1 1 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	Attribute\Port#  Tx Mux Statistics Received T1/E1 Bytes Skipped T1/E1 Bytes Idle Channel Count Generated HO STS/VC C Generated OC-3/STM-1 TxEngine free bytes TxEngine used bytes TxEngine used bytes Tx Mux Alarms OC-3/STM-1 Frame Gene Input Alarms Skipped Bytes Alarms No HO STS/VC Gen Alarms	1 429 919 834 112 0 2 127 998 343 709 332 781 N/A N/A Alarm Alarm	1 429 919 834 1 2 127 998 2 709 332 7 N Ala Ala
ttribute \Port #  keceived (calculated)  rames rame Len Errors kequence Errors H1 Errors PGA (hardware) X App Packets X App Packets X App Ackets X App Ackets X App Ackets X App Bytes SONET/SDH Info XX Unaligned Frames IDP Rx Datagrams X Buffer Overflows Invalid Udp LoPorts Mux Errors	Reset port 1 667 350 502 0 667 350 502 0 667 350 502 0 667 350 502 0 1 621 661 717 430 1 621 661 719 860 0 41 709 408 0 0 0 0 0 0 0 0 0	667 350 502 0 0 0 667 350 502 0 667 350 502 0 1 621 661 722 290 1 621 661 722 290 1 621 661 722 290 0 41 709 408 0 0 0	Attribute\Port#      Rx Demux Frames Received      STM-1 Frames      High-Order VC Frames      Processed OC-3/STM-1 Mul      Extracted T1/E1 Bytes <b>-Rx Demux Errors</b> Misaligned OC-3/STM-1 Fra      Non Tug2 Ho-STS/VC Payload      Unknown OC-3/STM-1 Fra      High-Order MF Add Count      Discarded STS/VC Frames      MF SYNC Loss Count      B3 Parity Count      B3 Parity Count      Rx Differ Overflow Count <b>-Rx Demux Alarms</b> No Frames Received      Unaligned OC-3/STM-1 Fra      Unknown OC-3/STM-1 Fra      Unknown STS/VC Payload      STS/VC MF Add	709 321 261 2 127 953 685 5 31 988 419 1 429 899 633 792 0 0 0 1 6 6 6 6 3 365 3 3 Alarm	709 316 270 2 127 938 811 531 984 702 1 429 899 674 112 0 0 0 1 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	Attribute\Port#  Tx Mux Statistics Received T1/E1 Bytes Skipped T1/E1 Bytes Idle Channel Count Generated HO STS/VC C Generated OC-3/STM-1 TxEngine free bytes TxEngine used bytes TxEngine used bytes Tx Mux Alarms OC-3/STM-1 Frame Gene Input Alarms Skipped Bytes Alarms No HO STS/VC Gen Alarms	1 429 919 834 112 0 2 127 998 343 709 332 781 N/A N/A Alarm Alarm	1 429 919 834 1 2 127 998 2 709 332 7 N Ala Ala
tribute\Port# Received (calculated) rames rame Len Errors sequence Errors B1 Errors PGA (hardware) Tx App Packets Tx FPGA Underruns tx App Packets Rx FPGA Overruns Tx App Bytes Xx App Bytes Stx App Bytes Sty App	Reset port 1 667 350 502 0 667 350 502 0 667 350 502 0 667 350 502 0 1 621 661 717 430 1 621 661 719 860 0 41 709 408 0 0 0 0 0 0 0 0 0	667 350 502 0 0 0 667 350 502 0 667 350 502 0 1 621 661 722 290 1 621 661 722 290 1 621 661 722 290 0 41 709 408 0 0 0	Attribute\Port# - Rx Demux Frames Received STM-1 Frames High-Order VC Frames Processed OC-3/STM-1 Mul Extracted T1/E1 Bytes - Rx Demux Errors Misaligned OC-3/STM-1 Fra Non Tug2 Ho-STS/VC Payload Unknown OC-3/STM-1 Fra High-Order MF Add Count Discarded STS/VC Frames MF SYNC Loss Count B2 Parity Count B3 Parity Count B3 Parity Count Rx Buffer Overflow Count - Rx Demux Alarms No Frames Received Unaligned OC-3/STM-1 Fra Unknown OC-3/STM-1 Fra Unknown OC-3/STM-1 Payl Unknown STS/VC Payload STS/VC MF Add Bytes not Extracted	709 321 261 2 127 953 685 5 31 988 419 1 429 899 633 792 0 0 0 1 1 6 6 6 6 6 6 6 3 355 3 3 8 4larm - - - - - -	709 316 270 2 127 938 811 5 31 984 702 1 429 899 674 112 0 0 0 1 1 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	Attribute\Port#  Tx Mux Statistics Received T1/E1 Bytes Skipped T1/E1 Bytes Idle Channel Count Generated HO STS/VC C Generated OC-3/STM-1 TxEngine free bytes TxEngine used bytes TxEngine used bytes Tx Mux Alarms OC-3/STM-1 Frame Gene Input Alarms Skipped Bytes Alarms No HO STS/VC Gen Alarms	1 429 919 834 112 0 2 127 998 343 709 332 781 N/A N/A Alarm Alarm	1 429 919 834 1 2 127 998 2 709 332 7 N Ala Ala
ttribute \Port #  Received (calculated)  rames rame Len Errors equence Errors PGA (hardware)  x App Packets x App Packets x App Packets x FPGA Overruns x App Bytes SONET/SDH Info tx Unaligned Frames JDP Rx Datagrams x Buffer Overflows nvalid Udp LoPorts Iux Erros	Reset port 1 667 350 502 0 667 350 502 0 667 350 502 0 667 350 502 0 1 621 661 717 430 1 621 661 719 860 0 41 709 408 0 0 0 0 0 0 0 0 0	667 350 502 0 0 0 667 350 502 0 667 350 502 0 1 621 661 722 290 1 621 661 722 290 1 621 661 722 290 0 41 709 408 0 0 0	Attribute\Port#  Rx Demux Frames Received STM-1 Frames High-Order VC Frames Processed OC-3/STM-1 Mul Extracted T1/E1 Bytes  Rx Demux Errors Misaligned OC-3/STM-1 Fra High-Order MF Add Count Discarded STS/VC Prames MF STNC Loss Count B2 Parity Count B3 Parity Count B3 Parity Count B3 Parity Count B3 Parity Count Rx Buffer Overflow Count <b>Fx Demux Alarms</b> No Frames Received Unaligned OC-3/STM-1 Fra Unknown OC-3/STM-1 Fra Unknown OC-3/STM-1 Fra Unknown OC-3/STM-1 Pra Unknown STS/VC Payload STS/VC MF Add Bytes not Extracted HO STS/VC not Received	709 321 261 2 127 953 685 5 31 988 419 1 429 899 633 792 0 0 0 1 6 6 6 6 3 365 3 3 Alarm	709 316 270 2 127 938 811 531 984 702 1 429 899 674 112 0 0 0 1 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	Attribute\Port#  Tx Mux Statistics Received T1/E1 Bytes Skipped T1/E1 Bytes Idle Channel Count Generated HO STS/VC C Generated OC-3/STM-1 TxEngine free bytes TxEngine used bytes TxEngine used bytes Tx Mux Alarms OC-3/STM-1 Frame Gene Input Alarms Skipped Bytes Alarms No HO STS/VC Gen Alarms	1 429 919 834 112 0 2 127 998 343 709 332 781 N/A N/A Alarm Alarm	1 429 919 834 1 2 127 998 2 709 332 7 N Ala Ala
ttribute \Port # Received (calculated) Recei	Reset port 1 667 350 502 0 667 350 502 0 667 350 502 0 667 350 502 0 1 621 661 717 430 1 621 661 719 860 0 41 709 408 0 0 0 0 0 0 0 0 0	667 350 502 0 0 0 667 350 502 0 667 350 502 0 1 621 661 722 290 1 621 661 722 290 1 621 661 722 290 0 41 709 408 0 0 0	Attribute\Port#  Rx Demux Frames Received  STM-1 Frames High-Order VC Frames Processed OC-3/STM-1 Mul Extracted T1/E1 Bytes  Rx Demux Errors Misaligned OC-3/STM-1 Fra High-Order MF Add Count Discarded STS/VC Payload Unknown OC-3/STM-1 Fra High-Order MF Add Count B3 Parity Count Rx Buffer Overflow Count • Rx Demux Alarms No Frames Received Unaligned OC-3/STM-1 Fra Unknown OC-3/STM-1 Fra Unknown STS/VC Payload STS/VC MF Add Bytes not Extracted HO STS/VC not Received HO STS/VC Discarded	709 321 261 2 127 953 685 531 988 419 1 429 899 633 792 0 0 0 1 1 6 6 6 6 6 3 365 3 3 4larm - - - - - - - -	709 316 270 2 127 938 811 531 984 702 1 429 899 674 112 0 0 0 1 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	Attribute\Port#  Tx Mux Statistics Received T1/E1 Bytes Skipped T1/E1 Bytes Idle Channel Count Generated HO STS/VC C Generated OC-3/STM-1 TxEngine free bytes TxEngine used bytes TxEngine used bytes Tx Mux Alarms OC-3/STM-1 Frame Gene Input Alarms Skipped Bytes Alarms No HO STS/VC Gen Alarms	1 429 919 834 112 0 2 127 998 343 709 332 781 N/A N/A Alarm Alarm	1 429 919 834 1 2 127 998 2 709 332 7 N Ala Ala
ttribute \Port # Received (calculated) Recei	Reset port 1 667 350 502 0 667 350 502 0 667 350 502 0 667 350 502 0 1 621 661 717 430 1 621 661 719 860 0 41 709 408 0 0 0 0 0 0 0 0 0	667 350 502 0 0 0 667 350 502 0 667 350 502 0 1 621 661 722 290 1 621 661 722 290 1 621 661 722 290 0 41 709 408 0 0 0	Attribute\Port#  Rx Demux Frames Received STM-1 Frames High-Order VC Frames Processed OC-3/STM-1 Mul Extracted T1/E1 Bytes  Rx Demux Errors Misaligned OC-3/STM-1 Fra High-Order MF Add Count Discarded STS/VC Prames MF STNC Loss Count B2 Parity Count B3 Parity Count B3 Parity Count B3 Parity Count B3 Parity Count Rx Buffer Overflow Count <b>Fx Demux Alarms</b> No Frames Received Unaligned OC-3/STM-1 Fra Unknown OC-3/STM-1 Fra Unknown OC-3/STM-1 Fra Unknown OC-3/STM-1 Pra Unknown STS/VC Payload STS/VC MF Add Bytes not Extracted HO STS/VC not Received	709 321 261 2 127 953 685 5 31 988 419 1 429 899 633 792 0 0 0 1 1 6 6 6 6 6 6 6 3 3 565 3 3 8 4larm - - - - - - -	709 316 270 2 127 938 811 5 31 984 702 1 429 899 674 112 0 0 0 1 1 3 3 3 3 3 3 3 3 3 3 3 3 3 3 3	Attribute\Port#  Tx Mux Statistics Received T1/E1 Bytes Skipped T1/E1 Bytes Idle Channel Count Generated HO STS/VC C Generated OC-3/STM-1 TxEngine free bytes TxEngine used bytes TxEngine used bytes Tx Mux Alarms OC-3/STM-1 Frame Gene Input Alarms Skipped Bytes Alarms No HO STS/VC Gen Alarms	1 429 919 834 112 0 2 127 998 343 709 332 781 N/A N/A Alarm Alarm	por 1 429 919 834 1 2 127 998 2 709 332 7 N N Ala Ala Ala



#### **SonetExpert™ Channelized Configuration Utility**

#### OC-3 Configuration Utility

- Sonet/SDH parameters
- OC-3/STM-1, T1 E1 ports, Channels and Mapping
- Clock setting of Sonet/SDH ports
- Cross port and loopback
   settings

Sonet Expert ConfigurationOC	-3 (C:\GlCommunications\S	-	
UDP Ports and Tx Event			OK
UDP remote port	20012		
UDP local port	20011		Expand All
Tx Event Name	SECH_TX_EVENT		
Shared Memory Name	SECH_SMEM		Collapse All
OC3, T1/E1 Ports, Channels a	nd Mapping		
OC-3 Ports	1		Set Default
OC-3 Streams	1		
T1/E1 Channels	1-63		Cancel
STS Mapping	E1_STS-1		
Channel Numbering Scheme	Lucent		
Enable Tx			
Broadcast Settings			
Enable Broadcast			
Broadcast Source Channel	1		
Broadcast Source OC-3	1		
Clock Settings			
Clock Port1	Internal		
Clock Port2	Internal		
Crossport Settings			
Crossport Port1	None		
Crossport Port2	None		
Loopback Settings			
Loopback Port1	None		
Loopback Port2	None		

#### UDP remote port

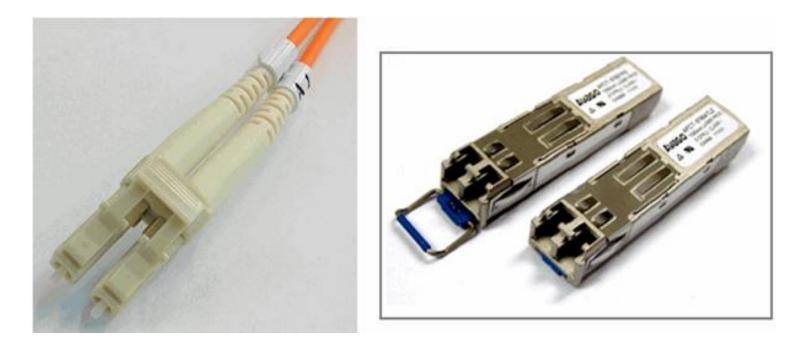
UDP remote port of STM T1/E1 Analyzer to send received STM1 T1/E1 multiframes to, default 20002

#### **STM-1** Configuration Utility

UDP Ports and Tx Event		OK
UDP remote port	20012	
UDP local port	20011	Expand A
Tx Event Name	SECH_TX_EVENT	
Shared Memory Name	SECH_SMEM	Collapse A
OC3, T1/E1 Ports, Channels an	nd Mapping	
STM-1 Ports	1	Set Defai
STM-1 Streams	1	Cancel
T1/E1 Channels	1-63	Lancer
VC Mapping	E1_VC3	
Channel Numbering Scheme	Lucent	
Enable Tx		
Broadcast Settings		
Enable Broadcast		
Broadcast Source Channel	1	
Broadcast Source STM-1	1	
Clock Settings		
Clock Port1	Internal	
Clock Port2	Internal	
Crossport Settings		
Crossport Port1	None	
Crossport Port2	None	
Loopback Settings		
Loopback Port1	None	
Loopback Port2	None	
<b>DP remote port</b> DP remote port of STM T1/E1 A		



#### **Optical Connectors and SFP Modules**



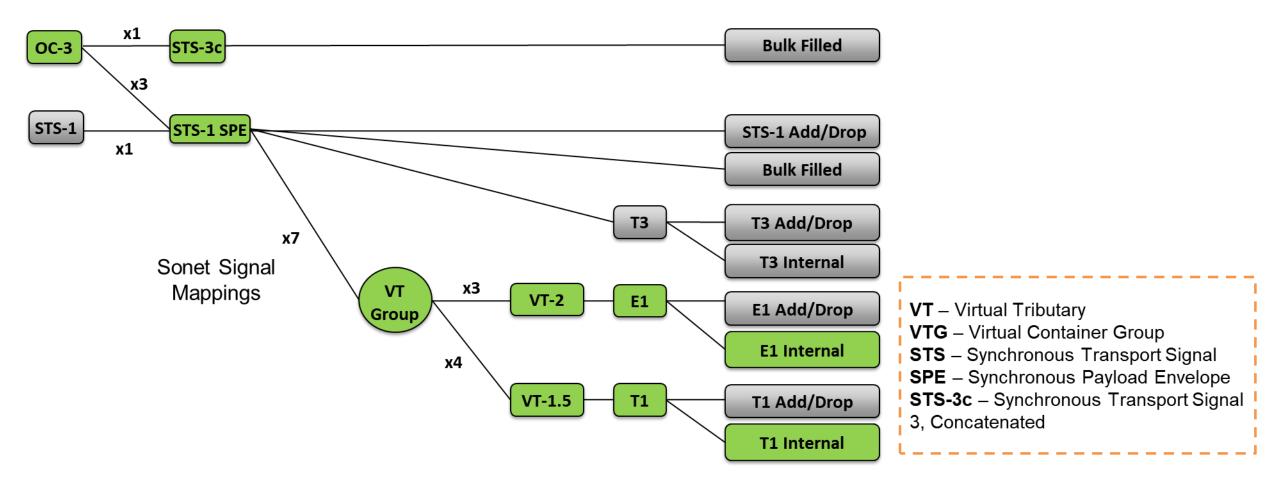
#### **LC Connectors**

#### 850 1310 1550 nm SFP Module



#### VC Mapping and Channel Numbering Scheme

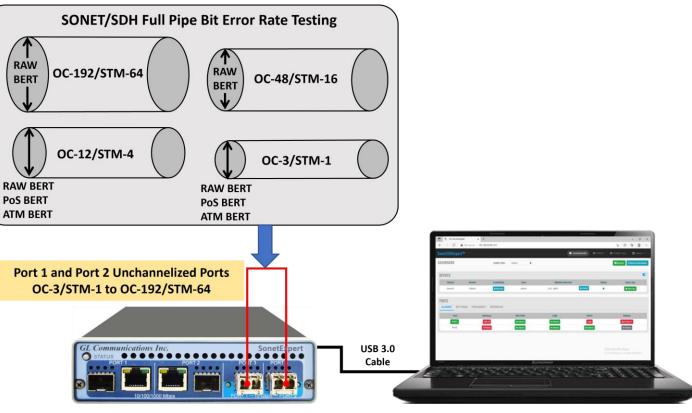
• The paths colored in green are currently supported on the GL's SonetExpert<sup>™</sup> hardware





#### **Unchannelized Analyzer**

- Wirespeed processing of ATM, PoS or RAW data for Tx and Rx for both ports
- Supports BERT testing at rates from OC-3/STM-1 to OC-192/ STM-64
- Ability to capture/playback to/from disk at full rate in both directions for all ports for detailed offline analysis
- Comprehensive transmit/receive testing capabilities; transmitting and verifying data with incrementing sequence numbers with each packet/cell
- Easy to use and flexible Bit Error Rate Test (BERT) application for ATM and POS
- ATM (AAL2, AAL5) Protocol Analyzer, UMTS Protocol Analyzer, PPP (IP and higher layer protocols) Protocol Analyzer
- ATM
  - > ATM Forum User Network Interface Specification
  - ATM physical layer for Broadband ISDN according to CCITT Recommendation I.432
- PPP over Sonet (PoS)
  - Point-to-Point Protocol (PPP) over Sonet/SDH specification according to RFC 2615 (1619) / 1662 of the PPP Working Group of the Internet Engineering Task Force (IETF)
- OC-3/STM-1, OC-12/STM-4 Transparent Payload
  - Analyzer processes Sonet/SDH payload in transparent (RAW) mode without any transport protocols



SonetExpert<sup>™</sup> Unchannelized Analyzer (Portable)

#### SonetExpert<sup>™</sup> Monitor and Control GUI Functionalities

- Starting and stopping the SEC service
- Configuring SEC service
- Launching Soft T1 E1 Analyzer
- Viewing and clearing the SEC service log
- Displaying alarms, error counters and operational statistics



# Thank you

