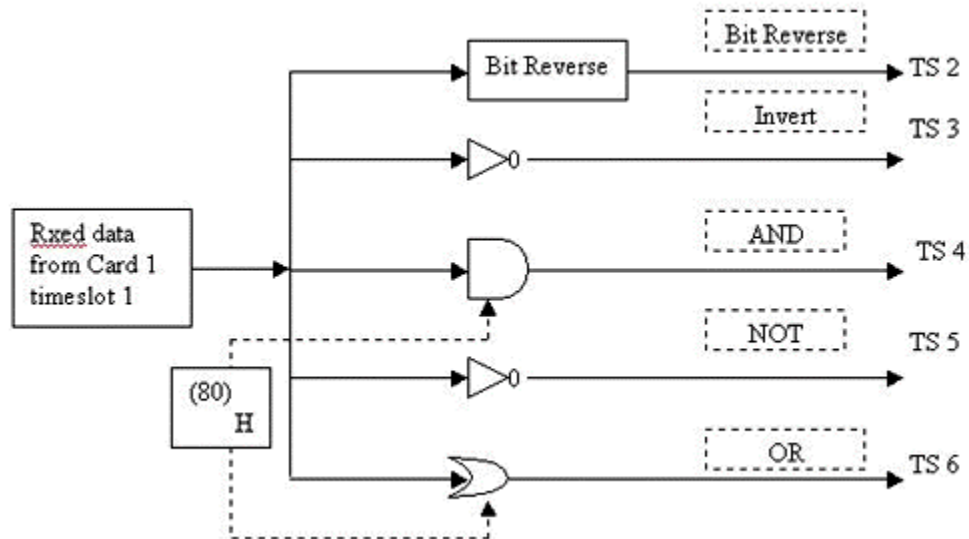


Testing Arithmetical functions on incoming bit stream



Script Description:

This script performs various types of arithmetical functions on the incoming bit stream.

Variation 1:

- Connect two PCs A and B . Run the script on PC A and verify the output on PC B. Do not run the GUI on PC A.
- Receive the data transmitted from card 1 timeslot 1 at card 2.
- 'Bit reverse' the received data at card2 timeslot 1 and transmit through card 2 timeslot 2.
- Invert the received data at card2 timeslot and transmit through card 2 timeslot 3.
- Perform 'AND' operation with (80)H and the received data at card2 timeslot 1 and transmit through card 2 timeslot 4.
- Perform 'NOT' operation with the received data at card2 timeslot 1 and transmit through card 2 timeslot 5.
- Perform 'OR' operation with (80)H and the received data at card2 timeslot 1 and transmit through card 2 timeslot 6.

Script:

```
// Connection assumed: connect card1 and card2 from PC 'A' to card1 and card2 of PC 'B' respectively.
```

```
// this script should not be run simultaneously with GUI
```

```
//MODE:SEQUENTIAL
```

```
//
```

```
// User needs to comment the commands according to his requirement
```

```
//
```

```
//BOARD SETTINGS...
```

```
//
```

```
//INTERFACE SETTINGS
```

```
set rx interface terminate #*;
```

```

//set rx interface monitor #*;
//set rx interface bridge #*;
wait 3000;
//
//FRAME FORMAT SETTINGS FOR T1
set superframe format esf #*;
//set superframe format d4 #*;
//
//FRAME FORMAT SETTINGS FOR E1
//set signaling mode cas #*;
//set signaling mode ccs #*;
//set crc4 on#*;
//set crc4 off #*;
//
//CLOCK SETTINGS
set tx clock source internal #*;
//set tx clock source recovered #*;
//set tx clock source external #*;
//
//LOOPBACK SETTINGS
//set inward driver loopback on #*;
//set outward driver loopback on #*;
set outward driver loopback off #*;
set inward driver loopback off #*;
wait 3000;
//
//- - - - - --Verification of initialization
//CHECKING FORMAT SETTINGS FOR T1
get superframe format #*;
//
//CHECKING FORMAT SETTINGS FOR E1
//get signaling mode #*;
//
//CHECKING OTHER SETTINGS FOR THE CARDS
get tx clock source #*;
get outward driver loopback #*;
get rx line frequency #*;
get rx line level #*;
wait 3000;
//
set latency 3;
set response 9;
//set priority default;
get response;
get latency;
get priority;
set latency default;
set response default;
//
//
dspop{ output1=brev(rx(#2:1)),output2=invert(rx(#2:1)),
output3=band(rx(#2:1),0x80),output4=bnot(rx(#2:1)), output5=bor(rx(#2:1),0x80),
tx(output1,#2:2),tx(output1,#2:2),tx(output2,#2:3),tx(output3,#2:4),tx(output4,#2:5),tx(output5
,#2:6)};
end task *;

```