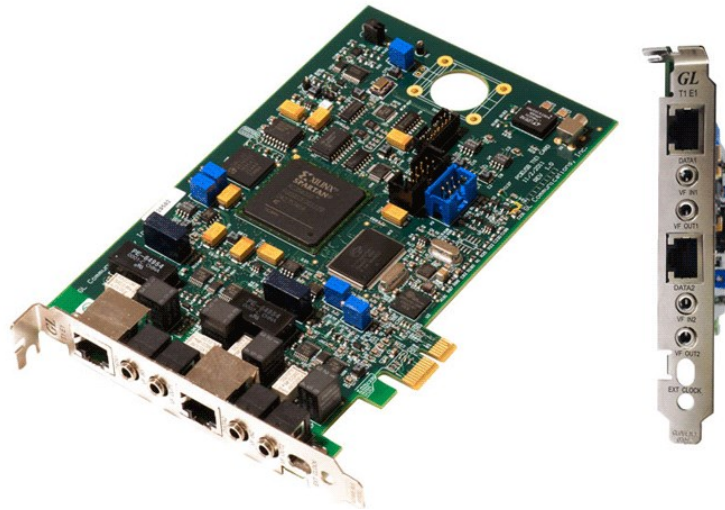


# Dual T1 E1 Express (PCIe) Analysis and Emulation Boards

(Analysis and Emulation Hardware)



## Overview

GL's **Dual T1 E1 Express (PCIe) Boards** are high-density dual T1 or E1 boards with newer PCIe (x1) bus interface. These cards are identical to the portable tProbe™ units, except for FXO FXS and Datacom functionality.

In addition to the other basic T1 or E1 line signals monitoring applications, the Dual T1 E1 Express (PCIe) Boards also support enhanced VF drop and insert capabilities with software selectable VF Tx and Rx impedances (135 $\Omega$ , 150  $\Omega$ , 600  $\Omega$ , 900  $\Omega$ , or High), Pulse Mask Compliance Testing, Jitter Generation and Measurement applications.

Supports all [Basic](#) and [Optional](#) applications that are available for tProbe™ T1 E1 analyzer including comprehensive Analysis / Emulation of Voice, Digits, Tones, Fax, Modem, Raw Data, Protocol, Analog, Digital, and Echo Testing.

For more details, visit [Dual T1E1 Express \(PCIe\) Analysis and Emulation Boards](#) webpage.

## Main Features

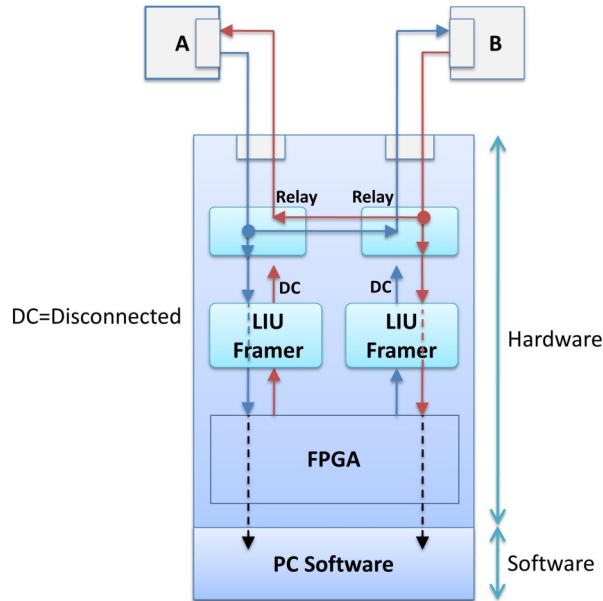
- Software Selectable T1 or E1 interfacing along with Drop and Insert
- PCI Express x1 Lane/Board compatible with Windows® 10 OS and user friendly real-time software
- Monitor the T1 or E1 line conditions such as frame errors, violations, alarms, frequency, power level, and clock (or frame/bit) slips
- TDM, ISDN, SS7 – High Density Voice
- VoIP, Frame Relay, Multilink Frame Relay, PPP and Multilink PPP, HDLC
- Routing and Bridging emulation over Multi T1 or E1 WAN interfaces using MLPPP (Multi Link PPP) and MFR (Multi Link Frame relay) protocols
- Comprehensive Analysis / Emulation of Voice, Data, Fax, Protocol, Analog, and Digital signals, including Echo and Voice Quality testing and supports T1 E1 Pulse Mask and Jitter Generation and Measurement Analysis
- "Cross-port Through" and "Cross-port Transmit" Modes – these configurations make cabling with Drop/Insert and Fail-Safe Inline Monitoring very easy
- Enhanced [VF Drop and VF Insert Capabilities](#) using 3.5mm Balanced (Stereo), or Unbalanced (Mono) physical connections.
- Windows and Linux Drivers for Open Source Applications and includes individual speakers (one speaker per card)
- Supports software selectable VF Tx and Rx impedances - 135 $\Omega$ , 150 $\Omega$ , 600 $\Omega$ , or 900 $\Omega$
- Additionally, the VF Rx impedance supports New High Impedance Monitor Termination (>50K  $\Omega$ ), external Microphone and Headset (Mic/HS) impedance
- VF Tx Gains ranges from –12 dB to +59.5 dB in 0.5 dB steps, and VF Rx Gains ranges from 63.5 dB to +9 dB in 0.5 dB steps
- Call Recording, Generation and monitoring for hundreds to thousands of calls in one platform



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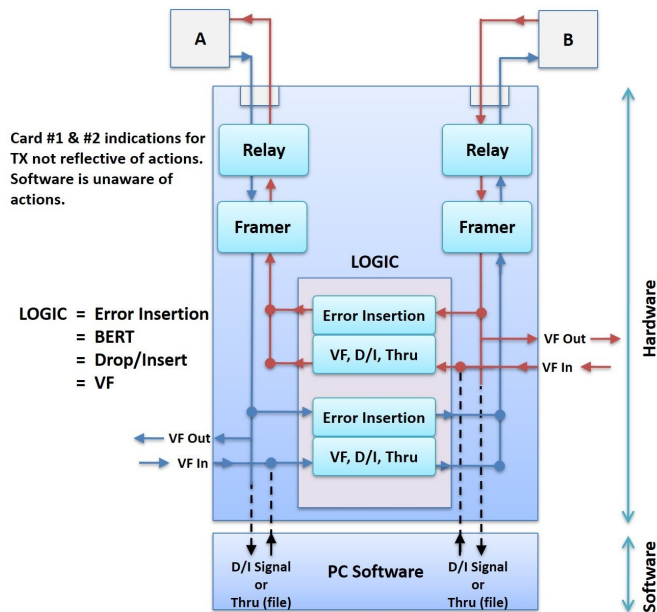
## Cross-port Through Loopback

This mode is similar to the standard Outward Loopback except that the signal received on Card 1 (Port 1) is transmitted out onto Card 2 (Port 2). Likewise the signal received on Card 2 (Port 2) is transmitted out onto Card 1 (Port 1). This feature allows monitoring T1 or E1 lines in-line while still being protected from loss of power to the board. This mode is effected entirely through relays and eliminates complex cabling.



## Cross-port Transmit Mode Loopback

In this mode, the data that would normally be transmitted on Card 1 (Port 1) is diverted and transmitted on Card 2 (Port 2) and the data that would normally be transmitted on Card 2 (Port 2) is diverted and transmitted on Card 1 (Port 1). The receive paths are completely unaffected. This mode is particularly useful for Drop and Insert and Error Injection applications in which the board analyzes and may insert traffic running between two pieces of T1/E1 equipment. This feature also eliminates complex cabling.



## Basic Software

- VF Options
  - Speaker
  - Drop and Insert
  - VF In / Out TS settings
- Monitoring Options
  - Monitor T1 or E1 Line
  - Byte Values and Binary Byte Values
  - Signaling bits, Power Level, DC Offset and Frequency
  - Multi-frames and Real-time Multi-frames
  - T1 or E1 Data as Real-time Bitmap
  - Time-slot Window
  - ASCII Timeslot Display
  - Oscilloscope and Power Spectral
  - Audio Monitoring
  - Active Voice Level
- Intrusive Testing
  - Bit Error Rate Test
  - Enhanced Bit Error Rate
  - ATM BERT
  - Transmit Tone
  - Transmit Gaussian Noise
  - Transmit Multiframe
  - Transmit Signaling Bits
  - Precision Delay Measurement
  - Rx-to-Tx Loop back
  - Error Insertion

## Optional Software

- Protocol Analysis
  - ISDN, HDLC, SS7, GSM, GPRS, UMTS, GR303, V5.x
  - Frame Relay, ATM, PPP, TRAU, CDMA, DCME, T1
  - E1 Maintenance Data Link (Sa HDLC and SSM), Facility Data Link, SS1, Fax, Modem
- Protocol Emulation
  - ISDN, SS7, ISUP Conformance Scripts, GSM Abis,
  - GSM A, MAP, CAP, INAP, FXO FXS, MLPPP, CAS,
  - TRAU, SS1, Multi-link Frame Relay Emulation
  - Inverse Multiplexing over ATM
- Windows Client / Server
  - w/ Remote access to T1 or E1 server using Clients - C++, TCL, C#
- Record / Playback Files
  - Manual, Automated
- Capture, Analysis, and Emulation
  - DTMF / MF / MFCR2, Digits, Tones, Voice, Fax, Modem, Raw Data
- Voice Band Analysis Software
- Call Data Records
- Multi-Channel BERT
- Protocol Identifier, Traffic Classifier
- Echo Cancellation Testing / Compliance
  - -Manual, Semi-automated and Automated –G.168, G.160, G.169
  - Measure Loop Delay/ERL
  - Delay Attenuate Timeslots
  - Digital Echo Canceller Simulator
  - Audio Processing Utility (APU)
  - Signaling Transitions Recording
- Real-time Strip Chart
- Real-time Multichannel Audio Bridge
- Multiplex / Demultiplex Software
- Network Surveillance, Voice Quality Testing

## Dual T1E1 Express (PCIe) – Specifications

### Physical Interface

T1 or E1	Dual RJ48c Connectors
Audio Signal	(4) 3.5 mm Balanced (Stereo) or Unbalanced (or Mono) Audio Jacks (Tx and Rx)
PC Interface	PCI Express x1 Lane Compliant to PCI Express Base Specification v1.1
External Clock Connector	(1) MCX Coaxial Jack

### Environmental Specifications

Temperature	Operating: 0 to 50° C Storage: -50 to 70° C
Relative Humidity	Operating: 10% to 90% (non-condensing) Storage: 0% to 95% (non-condensing)
Altitude	Operating: -100 to 12,000 ft. Storage: -100 to 40,000 ft.

### Physical Dimensions

Dimensions	6.7 inches x 4.4 inches.
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### T1 or E1 Line Interface

Framing Formats	Unframed, D4 (T1) , ESF(T1), ESF(J1), CAS(E1), FAS(E1), CRC4 Hardware Compliant: SLC96, T1ESF ZBTSI
Line Code format	AMI, B8ZS (T1) or HDB3 (E1)
Internal Clock Specification	Standard: +/- 3ppm Optional: +/- 1ppm
Output Clock Source	Internal (+/- 1 ppm or 3 ppm), Recovered, External Clock
T1 Output Level	T1: 3.0V Base to Peak Selectable 0-655Ft Pulse Equalization Setting; Tx Capability - DSX-1 Outputs (to 655 feet)
E1 Output Level	E1: 3.0V ±0.3V Base to Peak
Input Level	75 mV to 6V base to peak or -30 dBsx to -6 dBsx.
Line Built-Out Selections	0dB, -7.5dB, -15dB, -22.5dB for T1 only
Loopback	Normal (Outward and Inward) Cross-Port Transmit Loopback Cross-Port Through Loopback

## PCM Interface

Transmit	Synthesized Tone: 15 Hz to 3975 Hz selectable in 1Hz steps, +3.0dBm to -40dBm in 0.1 steps selectable, Frequency sweep. Dual Tone: Single or any combination of tones. Supervision: User defined states of A, B, (C, D) bits. Signaling: DTMF/MF Dialing Digits. File Playback: User created or recorded file. Special Codes:
Receive	Displays for All Channels: Signaling Bits, Power Level, Frequency, and Data. Graphical displays: Oscilloscope, Spectral, Spectrogram, Signal-to-Noise. Signaling: DTMF/MF Dialed Digit Detection and Analysis, ISDN, MFC-R2. Recorder: Record Full/Fractional T1/E1 Timeslots to hard disk file

## Transmit

T1 or E1 Interface Hardware Compliance	ANSI: T1.403.1995, T1.231-1993, T1.408 AT&T: TR54016, TR62411 ITU: G.703, G.704, G.706, G.736, G.775, G.823, G.932, I.431, O.151, Q.161 ITU-T: Recommendation I.432-03/93 B-ISDN User-Network Interface-Physical Layer Spec ETSI: ETS 300 011, ETS 300 166, ETS 300 233, CTR12, CRT4 Japanese: JTG.703, JTI.431, JJ-20.11 (CMI Coding Only).
BERT Pattern Generation	Pseudorandom patterns: (63) $2^6-1$ , (511) $2^9-1$ , (2047) $2^{11}-1$ , (32767) $2^{15}-1$ , (1048575) $2^{20}-1$ , (8388607) $2^{23}-1$ , QRSS. Hardware Compliant: T1 In-Band Loop Code Generation and Detection Fixed patterns: All Ones, All Zeros, 1:1, 1:7, 3 in 24, User Defined 24- Bits Hardware Compliant: User pattern of up to 32 bits in length.
Alarm Insertion	Blue, Yellow, Remote, Distant Multiframe Hardware Compliant: Bit 7 Zero Suppression D4 Yellow: 1 in S bit of frame 12, AIS-CI Code, ESF-RAI CI Code Receive Carrier Loss: 0's for 2047 or 255 bits (For E1 only).
Error Insertion	BPV, Bit Error, Frame Error, CRC Errors, Burst Frames, Fixed Error Rate, Random Error Rate, auto logic from $10^{-2}$ to $10^{-9}$ for selectable 56K or 64Kps channels
Drop and Insert	Any contiguous set of digital timeslots and/or audio input
Facility Data Link	T1 ESF Mode: Transmit/Receive Messages, Bit-Oriented Messages, and Files
Zero Suppression	B7 Stuffing, Transparent, & B8ZS (T1)
Signaling	Robbed-Bit or Clear Channel
Frequency Offset	T1: +/- 615 Hz E1: +/- 615 Hz.

## Receive

Input Impedance	100Ω for Terminate and Monitor (T1) 120Ω for Terminate and Monitor (E1) >1KΩ for Bridge
Terminations	Terminate, Monitor, Bridge
T1 Input Frequency	1.544MHz +/- 20 KHz.
E1 Input Frequency	2.048Mhz +/- 20 KHz.
Frequency Measurement	+/- 1ppm
Error Detection	Frame Error, CRC Error, BPV Error, Logic Error, Frame Alignment Error Hardware Compliant: *10 or 24 bits for sync time *2/4, 2/5, or 2/6 frame bit in error frame select *Frame error bit corruption for 1 or 3 frame bits *E-Bit Error *Line Code Violation
Alarm Detection	T1 – D4 Yellow Alarm, ESF Yellow Alarm Yellow Alarm (B2 Suppressed-2nd MSB) Yellow Alarm (S-Bit) Yellow Alarm (00FF in FDL) Blue Alarm (Framed or Unframed All Ones) E1 - Remote Alarm Distant Multi-Frame Alarm Signaling All Ones Unframed All Ones Hardware Compliant: J1 Yellow Alarm
Intrinsic Jitter	Meets Jitter Tolerance: Meets AT&T TR 62411 (Dec. 90) Meets ITU-T G.823 Jitter Transfer: Meets AT&T TR 62411 (Dec. 90)
Input Range	T1: Terminate, 0 to 36dB (Long Haul), DSX Monitor, Bridge Hardware Compliant: Terminate, 0 to 15dB (Limited Long Haul), DSX Monitor 20 dB, 26 dB, 32 dB E1: Terminate, 0 to 43dB (Long Haul), DSX Monitor, Bridge Hardware Compliant: Terminate, 0 to 13 db

## Display and Logging

BERT	Bit Errors, Bit Error Rate, Error Seconds, Error Free Seconds, %EFS, Severely Error Seconds, % SES, Degraded Minutes, %Dmin, Loss Pattern Sync Count, Loss of Sync Seconds, Available Seconds, %Available Seconds, Unavailable Seconds, Bipolar Violations, BPV Rate, BPV Seconds, BPV Free Seconds, Frame Errors, FE Rate, FE Seconds, FE Free Seconds, with Detailed logging into disk file
Alarms	Resync In Progress, Loss of Signal, Blue Alarm, Change of Frame Alignment, Bipolar Violation, Frame Error, Carrier Loss, Yellow Alarm, Out of Frame Events Counter, Error Super frame Counter, Bipolar Violations, Remote Alarm, Distant Multiframe Alarm, Signaling All Ones, CAS Multiframe Error, CRC4 Error

## VF Drop and Insert

Transmit	Signal Level Measurement Accuracy: 0.0dBm +/- 0.1dBm. Range: -7.2 dB to +18.2 dB selectable gain in 0.1 dB steps Output Impedance: 135Ω, 50Ω, 600Ω, 900Ω
Receive	Audio Monitoring: Built-in Speaker. Audio Insertion: Selected DS0 replaced with inserted audio from VF Input. Range: 18.0 dB to +7.3 dB selectable gain in 0.1 dB step. Volume Control: User specified software controller. Input Impedance: Software selectable 135Ω, 50Ω, 600Ω, 900Ω for Intrusive Testing. High Impedance (>50KΩ) for Non-Intrusive Testing. Provision for external Microphone (Mic/HS) on VF ports connection
Sampling Rates	8KHz, 16KHz
Datawidth (bits)	Supports 8, 16, 20, 24, 32 Bit Data
VF Tx Gains	Supports -12 dB to +59 dB in 0.5dB Steps Gain (0.1 dB steps can also be
VF Rx Gains	Supports -63.5 dB to +9 dB in 0.5dB Steps Attenuation (0.1 dB steps can also be accommodated)
Connectors	(4) 3.5 mm Balanced (Stereo) or Unbalanced (Mono) Audio Jacks (Tx and Rx)

## Buyer's Guide

Item No	Product Description
<a href="#">XTE001</a>	Dual T1 E1 Express (PCIe) Boards (requires additional licenses)
<a href="#">XUT001</a>	Dual T1 E1 Express Card Basic T1 Software (includes xx600, xx605)

For more details, visit [Dual T1E1 Express \(PCIe\) Analysis and Emulation Boards](#) webpage.



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